

ABSTRACT OF THE DISCLOSURE

The present invention provides a circuit for driving a flat panel display, including a storage capacitor, a transmission gate and a current-limiting transistor. A first terminal of the storage capacitor is coupled to a system voltage, and a second terminal of the 5 storage capacitor is at storage voltage. A first input/output terminal of the transmission gate is coupled to the storage voltage, and the second input/output terminal of the transmission gate is coupled to the data current source. A first gate terminal of the transmission gate is coupled to the first signal; a second gate terminal of the transmission gate is coupled to the second signal. A gate terminal of the current- 10 limiting transistor is coupled to the storage voltage, wherein a first source/drain terminal is coupled to the system voltage, and a second source/drain terminal provides current for driving the flat panel display.